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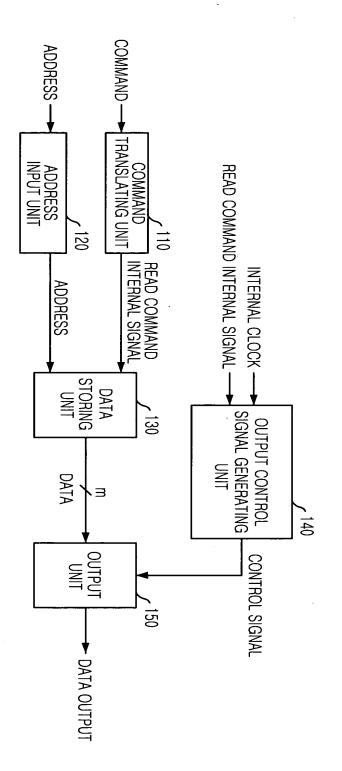
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FOR GENERATING IMPROVED OUTPUT CONTROL SIGNAL

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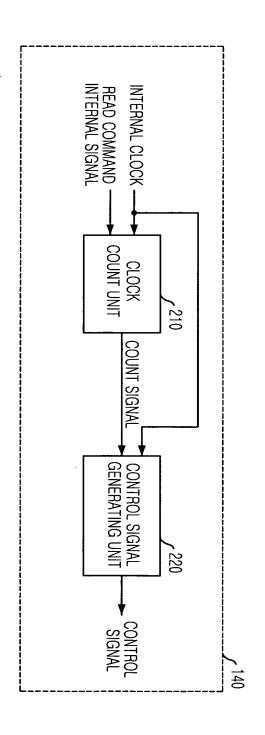


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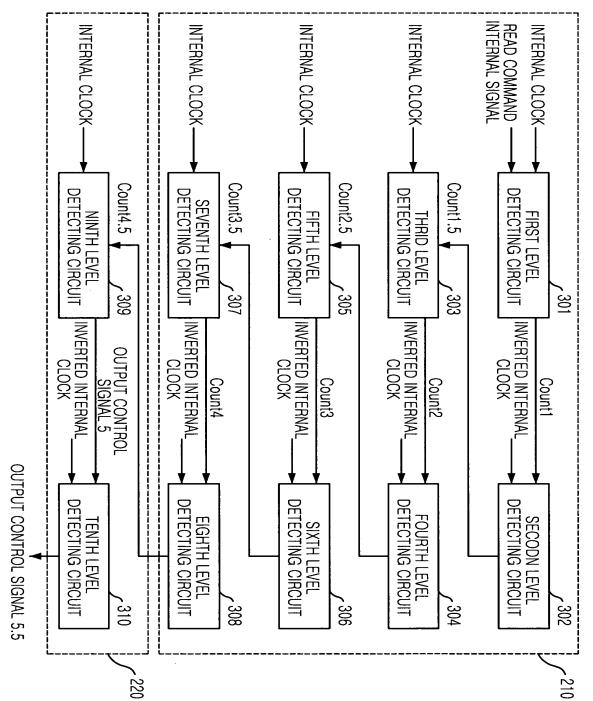
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FOR GENERATING IMPROVED OUTPUT CONTROL SIGNAL 1st Named Inventor: Jeong-Ho BANG Express Mail No.: EV339910178US Docket No.: 51876P430 Sheet: 4 of 10 INTERNAL CLOCK
INVERTED INTERNAL
CLOCK
READ COMMAND
INTERNAL SIGNAL
Count1 EXTERNAL CLOCK — Count4.5 Count1.5 Count4 Count3 Count2 Count2.5 Count3.5 READ COMMAND FIG. 4 (PRIOR ART)

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FOR GENERATING IMPROVED OUTPUT CONTROL SIGNAL 1st Named Inventor: Jeong-Ho BANG 1876P430 EXTERNAL CLOCK
SINVERTED INTERNAL
CLOCK
READ COMMAND
INTERNAL SIGNAL Express Mail No.: EV339910178US EXTERNAL CLOCK Sheet: 5 of 10 Count4.5 Count1.5
Count2.5
Count3.5
Count4 Count1 READ COMMAND FIG. 5 (PRIOR ART)

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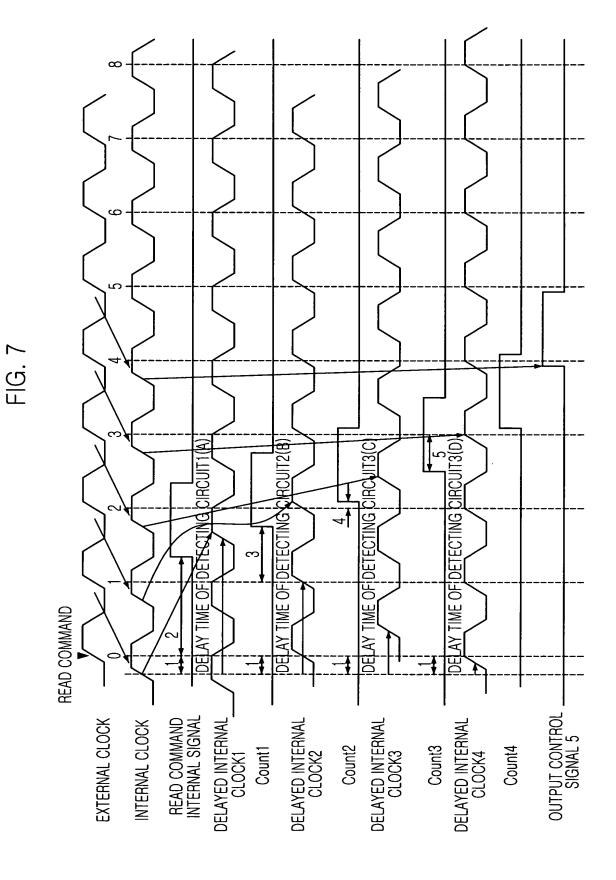
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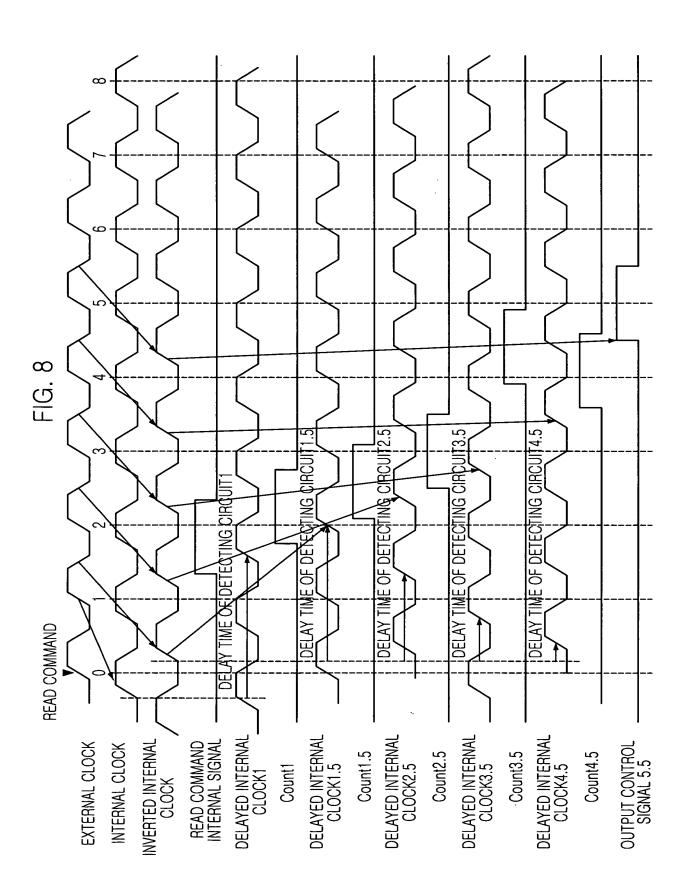
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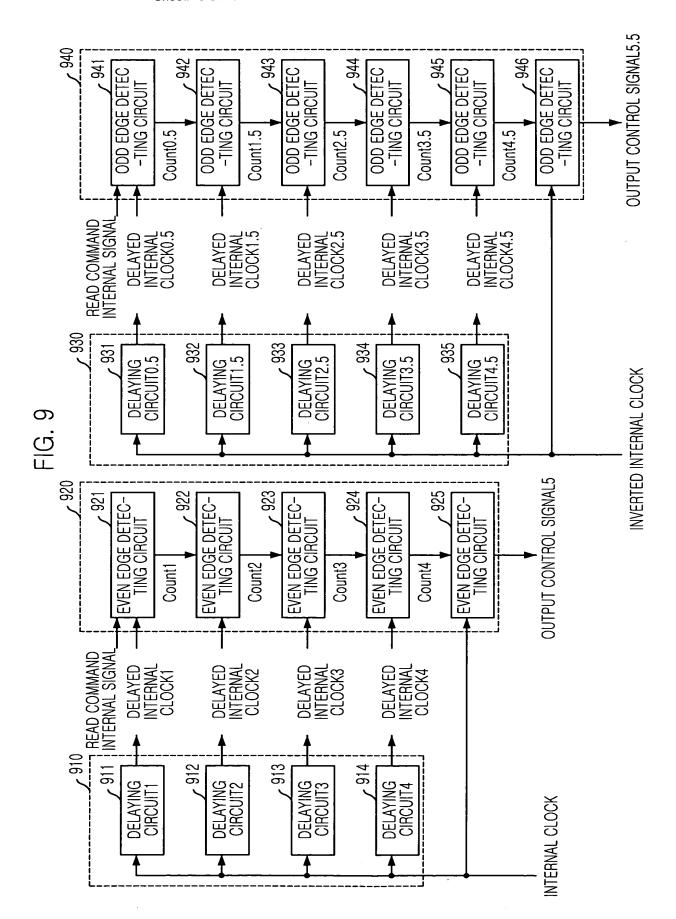


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